

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name		Classification
*	Α	US-4,523,104	06-1985	Norris et al.		327/386
*	В	US-4,549,094	10-1985	Floyd, William M.		327/386
*	Ç	US-3,792,466	02-1974	Arnold et al.		341/25
*	D	US-5,386,159	01-1995	Dupre, Jean L.		327/310
*	Е	US-3,886,543	05-1975	Marin, Robert E.		341/24
*	F	US-2002/0185128	12-2002	Theobald, Stephen		128/203.15
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	CD4015 4-bit Shift Register, National Semiconductor, 6/1006				
	v	DM74LS164 8-bit Serial In/ Parallel Out Shift Register, Fairchild Semiconductor, 4/2000				
	w	Definition of Shift Register, Wikipedia				
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.